


Q38	Q38	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
FA SCANNED <u>DK1</u> Q.A. <u>CS1</u>	

APPLICATION NO. 09/891324	CONT/PRIOR	CLASS 438 250	SUBCLASS 310	ART UNIT 2812 2812	EXAMINER 
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APPLICANTS

Osamu Nakagawa

438

2004

2512

SCH. - 21/2671

TITLE


Process for high-dielectric constant metal-insulator metal capacitor in VLSI multi-level metallization systems

PTO-2040
12/99

ISSUING CLASSIFICATION

ISSUING CLASSIFICATION											
ORIGINAL				CROSS REFERENCE(S)							
CLASS		SUBCLASS		CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)					
438		238		438		240		240		253	
INTERNATIONAL CLASSIFICATION											
401.6-21/8242											

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig. <div style="text-align: center;">4 7 1</div>			CLAIMS ALLOWED Total Claims Print Claim for O.G. <div style="text-align: center;">13 1</div>	
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. * _____ _____ _____	<div style="text-align: center;">  (Assistant Examiner) (Date) </div>			ISSUE FEE	
	_____ (Primary Examiner) (Date)			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	
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